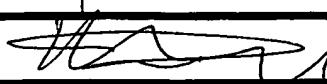


INFORMATION DISCLOSURE CITATION		Docket Number AD-356J		Application Number		
		Applicants Stein et al.				
		Filing Date September 17, 2003		Art Unit		
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<i>VL</i>	13	Viktor Fischer, <i>Realization of the Round 2 AES Candidates Using Altera FPGA</i> , (January 26, 2001) < http://csrc.nist.gov/CryptoToolkit/aes/round2/conf3/papers/24-vfischer.pdf > (Micronic - Kosice, Slovakia).				
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			Applicants Stein et al.			
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<i>WZ</i>	27	elixent Application Note <i>JPEG Codec</i> (Dec. 9, 2002) < http://www.elixent.com/assets/jpeg-coder.pdf > (elixent - Bristol, UK).				
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REF	FOREIGN DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	Translation	
				YES	NO
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EXAMINER			DATE CONSIDERED	<i>01/09/2006</i>	
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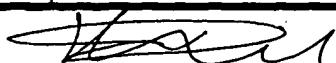
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